

Inventor: BEN ZE'EV et al. Docket No.: 9124.132USW1 Title. PA TKET PROCESSOR Serial No.: 10/038,697 Sheet 2 of 6

FIG.3

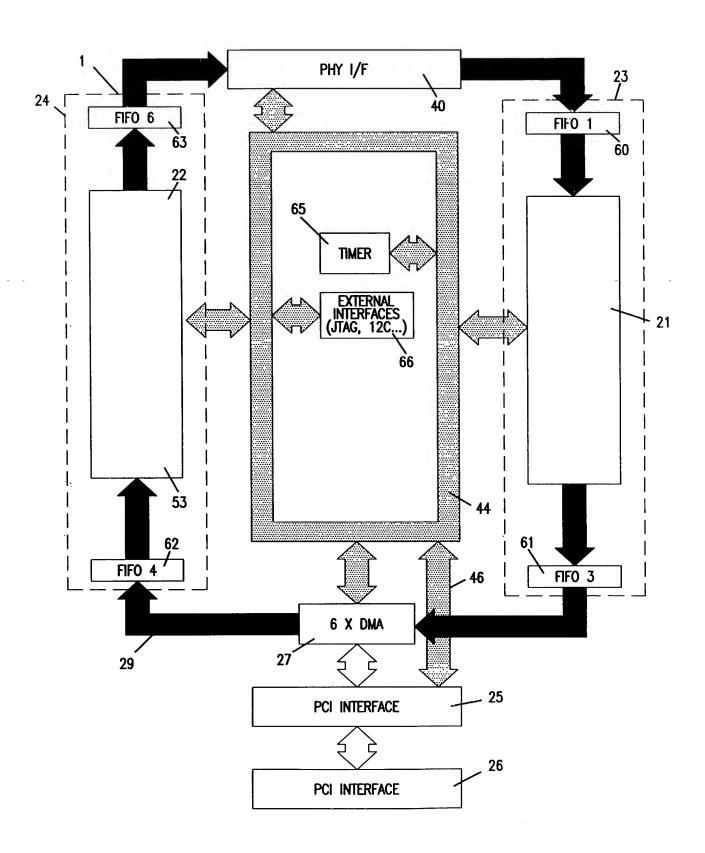
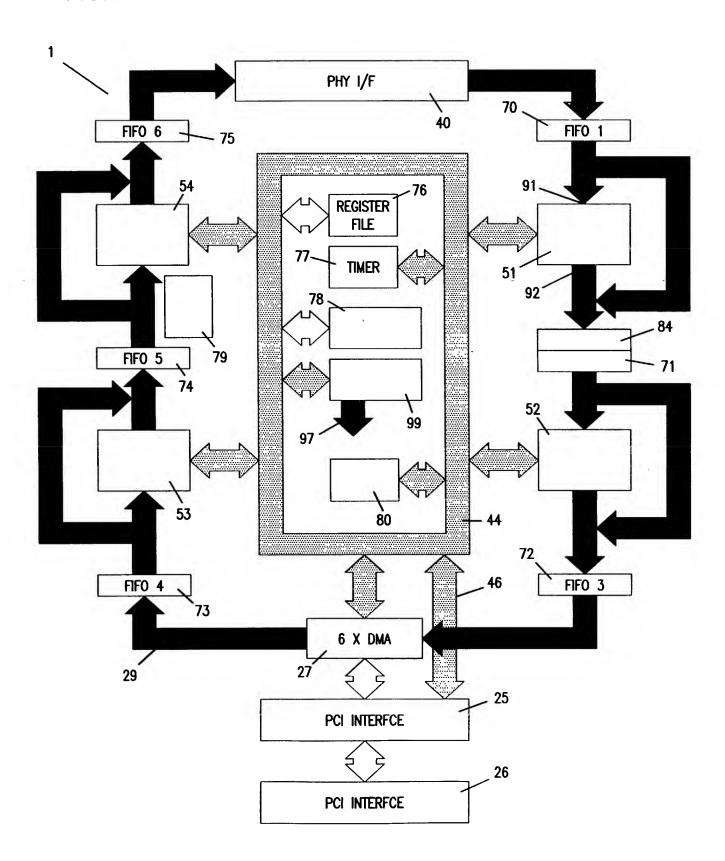
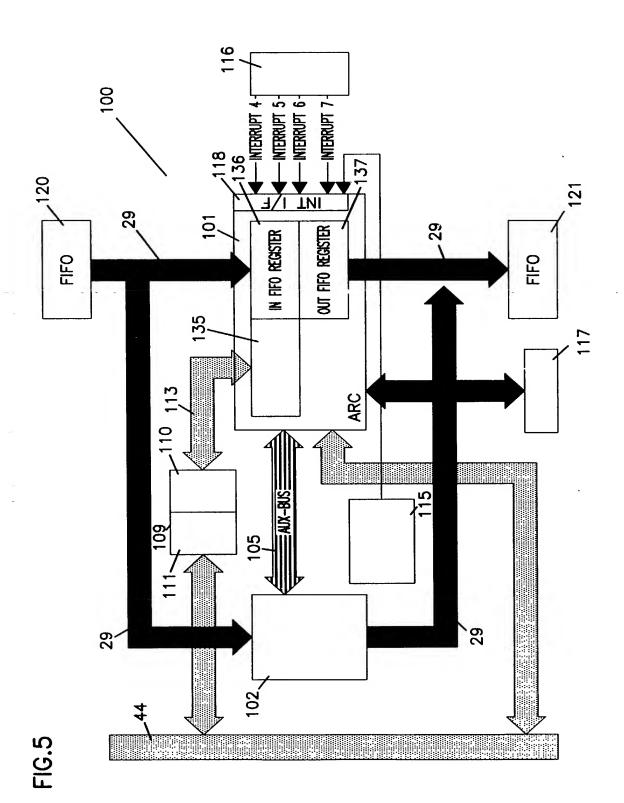
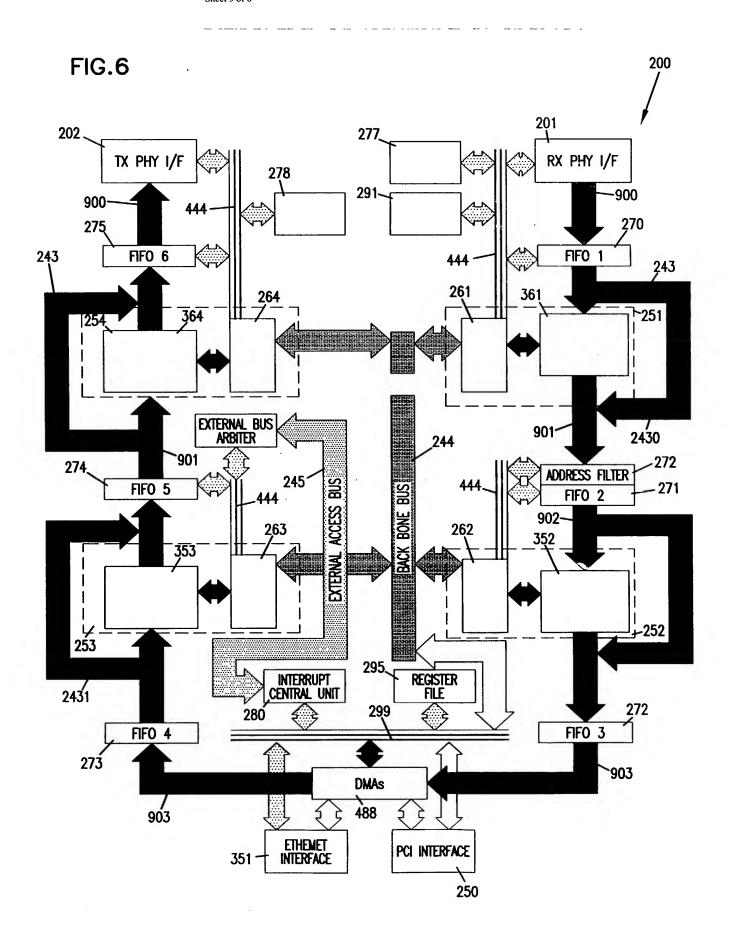


FIG.4





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290 PERIPHERAL DUAL PORT MEMORY CONTROLLER I-CACHE INTERRUPT I/F **4−JJAT**S--JJAT2 IN FIFO CORE REGISTER FF0 FIF0 -data rea<u>dy</u> -FIFO FULL-2430-ARC CORE LOES BUSY-TCRC BUSY-445 DES & CRC